

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

APPLICANTS: Jeoung-Mo KOO et al. EXAMINER: Jose R. Diaz  
SERIAL NO.: 10/799,201 GROUP ART UNIT: 2815  
FILED: March 12, 2004  
FOR: PROGRAMMABLE MEMORY DEVICE, INTEGRATED  
CIRCUIT INCLUDING THE PROGRAMMABLE MEMORY  
DEVICE, AND METHOD OF FABRICATING SAME

Commissioner for Patents  
P.O. Box 1450  
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**AMENDMENT AND RESPONSE TO OFFICE ACTION**

Sir:

This paper is submitted in response to the September 15, 2006 Office Action issued by the United States Patent and Trademark Office in connection with the above-identified application. A response to the September 15, 2006 Office Action is due on December 15, 2006. Therefore, this paper is being timely filed.